

# SANCHUAN CHEN

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## EDUCATION

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### **The Ohio State University**

*Aug. 2014 - Present*

Ph.D. student in Computer Science and Engineering  
Department of Computer Science and Engineering  
Advisors: Dr. Zhiqiang Lin, Dr. Yinqian Zhang

### **Institute of Software, Chinese Academy of Sciences**

*Aug. 2009 - Jan. 2014*

M.E. in Computer Software and Theory  
Department of Computer Science and Engineering

### **University of Science and Technology of China**

*Aug. 2005 - July 2009*

B.E. in Computer Software and Technology  
Department of Computer Science and Technology

## RESEARCH INTERESTS

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**Software Security**

**Programming Languages**

**Binary Analysis**

**Trusted Execution Environment**

## PUBLICATIONS

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### **SGX-Racer: Detecting Controlled Data Races in Enclave Binaries**

Sanchuan Chen, Zhiqiang Lin, Yinqian Zhang.  
In submission.

### **SelectiveTaint: Efficient Data Flow Tracking With Static Binary Rewriting**

Sanchuan Chen, Zhiqiang Lin, Yinqian Zhang.  
USENIX Security'21, Vancouver, B.C., Canada, Aug. 2021.

### **SgxPectre: Stealing Intel Secrets from SGX Enclaves via Speculative Execution**

Guoxing Chen, Sanchuan Chen, Yuan Xiao, Yinqian Zhang, Zhiqiang Lin, and Ten H. Lai.  
EuroS&P'19, Stockholm, Sweden, Jun. 2019.

### **Leveraging Hardware Transactional Memory for Cache Side-Channel Defenses**

Sanchuan Chen, Fangfei Liu, Zeyu Mi, Yinqian Zhang, Ruby B. Lee, Haibo Chen and XiaoFeng Wang.  
AsiaCCS18, Incheon, Korea, June 2018.

### **Racing in Hyperspace: Closing Hyper-Threading Side Channels on SGX with Contrived Data Races**

Guoxing Chen & Wenhao Wang, Tianyu Chen, Sanchuan Chen, Yinqian Zhang, XiaoFeng Wang, Ten-Hwang Lai, Dongdai Lin.  
Oakland'18, San Francisco, USA, May. 2018.

## **Stacco: Differentially Analyzing Side-Channel Traces for Detecting SSL/TLS Vulnerabilities in Secure Enclaves**

Yuan Xiao, Mengyuan Li, Sanchuan Chen, Yinqian Zhang .  
CCS'17, Dallas, USA, Oct. 2017.

## **Detecting Privileged Side-Channel Attacks in Shielded Execution with DÉJÀ VU**

Sanchuan Chen, Xiaokuan Zhang, Michael K. Reiter, Yinqian Zhang.  
AsiaCCS'17, Abu Dhabi, UAE, Apr. 2017.

## **RESEARCH EXPERIENCE**

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### **Detecting Controlled Data Races in Enclave Code**

The project investigates a new attack vector of Intel SGX, which is caused by non-reentrant enclave code that allows an attacker, e.g., a malicious OS, to trigger a controlled data race to breach the integrity of the enclaves execution and proposes a static binary analysis tool to identify the exploitable data races in enclave executable.

### **Cross-Architecture Binary Similarity Analysis**

The project uses the architecture-neutralized and optimization-resilient value sets written to each registers and memory cells at function exit point as a signature to capture the semantics of a function for similarity comparison.

### **Improving Performance of Data Flow Tracking**

The project aims at designing novel static binary analysis algorithm to identify instructions that will not be tainted at run-time to improve the performance of data flow tracking system such as libdft.

### **Detecting privileged Side channel attacks in Shielded Execution**

The project presents a software framework that enables a shielded execution to detect privileged side-channel attacks and we build into shielded execution the ability to check program execution time at the granularity of paths in its control-flow graph.

## **RESEARCH GRANT EXPERIENCE**

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Assisted in the preparation of the following research grant/gift proposal:

Type-aware recovery of symbol names in binary code: a machine learning based approach *2020*

Requested budget: \$80,000 + \$20,000 credits

Amazon Research Award (Awarded).

## **RESEARCH MONITORING EXPERIENCE**

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Mentored the research of two undergraduate students:

- Andrew Haberlandt (BS, OSU)
- Bo Lu (BS, OSU)

## **TEACHING EXPERIENCE**

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### **Lab Instructor**

CSE 2111: Modeling and Problem Solving with Spreadsheets and Databases *Aug. 2015 - May 2017*  
200 students, 6 terms

### **Graduate Teaching Assistant**

CSE 5331: Foundations II: Data structures and algorithms *Aug. 2014 - May 2015*  
40 students, 2 terms

## SERVICE EXPERIENCE

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<b>Reviewer</b>	<i>2020</i>
IEEE Transactions on Dependable and Secure Computing (TDSC)	<i>2020</i>
<b>Shadow Program Committee Member</b>	<i>2021</i>
IEEE Symposium on Security and Privacy (Oakland)	<i>2021</i>
<b>External Reviewer</b>	<i>2017-2020</i>
IEEE Transactions on Dependable and Secure Computing (TDSC)	<i>2019</i>
IEEE Symposium on Security and Privacy (Oakland)	<i>2017, 2021</i>
ACM Conference on Computer and Communications Security (CCS)	<i>2017, 2018, 2020</i>
USENIX Security Symposium (SEC)	<i>2017, 2021</i>
ISOC Network and Distributed System Security Symposium (NDSS)	<i>2019, 2020</i>
Annual Computer Security Applications Conference (ACSAC)	<i>2018, 2019, 2020</i>
ACM ASIA Conference on Computer and Communications Security (ASIACCS)	<i>2021</i>
International Conference on Dependable Systems and Networks (DSN)	<i>2020</i>
EAI International Conference on Security and Privacy in Communication Networks(SecureComm)	<i>2019, 2020</i>
Conference on Detection of Intrusions and Malware & Vulnerability Assessment (DIMVA)	<i>2019</i>
International Conference on Applied Cryptography and Network Security (ACNS)	<i>2020</i>
Annual Digital Forensics Research Conference (DFRWS)	<i>2019</i>
<b>Artifact Evaluation Member</b>	<i>2020</i>
Annual Computer Security Applications Conference (ACSAC)	<i>2020</i>

## MEDIA COVERAGE

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**“New Spectre derivative bug haunts Intel processors”**

by Andy Patrizio, Network World, March 7, 2018.(Link)

**“If there’s somethin’ stored in a secure enclave, who ya gonna call? Membuster!”**

by Thomas Claburn, The Register, December 5, 2019.(Link)

## AWARDS

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Student Travel Grant, AsiaCCS 2018	<i>2018</i>
Excellent Volunteer of 50th Anniversary of USTC, USTC	<i>2008</i>
Outstanding Student Scholarship Grade 2, USTC	<i>2008</i>
Outstanding Student Scholarship Grade 2, USTC	<i>2007</i>
Outstanding Student Scholarship Grade 3, USTC	<i>2006</i>
Outstanding Freshman Scholarship Grade 3, USTC	<i>2005</i>

## REFERENCES

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Duke University  
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Electrical & Computer Engineering  
LSRC Building D310  
308 Research Drive, Duke Box 90129  
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Southern University of Science and Technology  
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