Design and Evaluation of an RDMA-aware Data Shuffling Operator for Parallel Database Systems Feilong Liu, Lingyan Yin and Spyros Blanas The Ohio State University

Key contributions

An RDMA-aware data shuffling operator for parallel database systems

- > The endpoint abstraction hides RDMA details
- Multiple endpoints avoid thread contention



Two-sided Send/Receive and unreliable delivery fully utilize the network bandwidth, accelerate TPC-H by 2×

Why is data shuffling important?





Challenges

Key result

Isolate the complexity of RDMA

- Manage memory registration
- > Anticipate packets may arrive out of order > Consider both UD and RC transport

Support different implementations

Evaluation



Two communication patterns: Repartition & Broadcast

Isolate the complexity of RDMA



Identify promising design choices

Compare two-sided and one-sided primitives

Propose the endpoint abstraction:

- Hides the complexity of synchronization and memory management in RDMA communication
- One shuffle operator can have one or multiple endpoints
- All functions are thread-safe

The endpoint abstraction hides the complexity of RDMA

Identify promising design choices







No design choice is strictly	
better than the others	



Conclusions

- \succ Two-sided send/receive and unreliable delivery fully utilize the network bandwidth in a database system
- \succ We propose the endpoint abstraction to hide RDMA details
- > We design a shuffling operator with multiple endpoints to avoid thread contention, accelerate TPC-H queries by 2×

code.osu.edu/pythia



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